

(11) Publication number:

03091239 A

Generated Document

## PATENT ABSTRACTS OF JAPAN

(21) Application number: 01227351

(51) Intl. Cl.: H01L 21/28 H01L 21/205 H01L 21/285

(22) Application date: **04.09.89** 

(30) Priority:
(43) Date of application
16 04

16.04.91

(84) Designated contracting states:

publication:

(71) Applicant: HITACHI LTD

(72) Inventor: KOBAYASHI TAKASHI HIRAIWA ATSUSHI

IIJIMA SHINPEI

(74) Representative:

## (54) FORMATION OF THIN FILM

(57) Abstract:

PURPOSE: To contrive the reduction of the resistance of wirings by using a mixed gas as a material gas which includes disilane or trisilane and arsine.

CONSTITUTION: For a material gas, a mixed gas including Si2H6 or Si3H8 and AsH3 is used and an Si film is formed under a film forming temperature of 450°C-550°C. After

A KC71KNCN Tage Lore

that, the film is made into a polycrystalline film by heat treatment. This is a polycrystalline silicon film which includes crystal particles whose diameter is 10 times its film thickness or larger and whose surface has unevenness of 5nm or less, and also includes arsenic. As a result, such Si film can be formed that realizes high productivity and conductivity and allows little impurity diffusion into a substrate Si.

COPYRIGHT: (C)1991, JPO& Japio